

Docket No. IRV1.PAU.30



GP-3722  
Patent Application

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Douglas M. Albert

Serial No.: 09/190,378

Filed: November 10, 1998

For: METHOD FOR THINNING  
SEMICONDUCTOR WAFERS  
WITH CIRCUITS AND WAFERS  
MADE BY THE SAME

Examiner:

Group Art Unit:

Costa Mesa, California

April 9, 1999

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Group 3700

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Box DD  
Washington, DC 20231

Dear Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97(c) and 1.98, applicant wishes to bring to the attention of the U.S. Patent and Trademark Office the following references:

U.S. Patents

4,266,334	Edwards et al.
5,480,842	Clifton et al.
5,733,814	Flesher et al.

Other Documents

IBM Patent Server -- Search Results of October 24, 1998

MOORE'S LAW, The Next Dimension, by Sergey Savastiouk, Oleg Siniaguine & Mark DiOrio, September/October 1998, pages 55-58

Copies of these references are enclosed herewith, and are listed on the attached PTO Form 1449.

If the examiner believes that a telephone interview would help further the prosecution of this case, he is respectfully requested to contact the undersigned attorney at the listed telephone number.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on

April 9, 1999

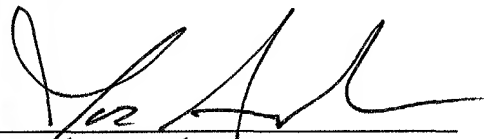
by Eric Hoover



Signature

April 9, 1999

Respectfully submitted,



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